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designed for the addition of binary number, but they can be used in various other applications like binary code decimal, address decoding, table index calculation, etc.

Half Adder

There are two inputs and two outputs in a Half Adder. Inputs are named as A and B, and the outputs are named as Sum (S) and Carry (C). The Sum is X-OR of the input A and B. Carry is AND of the input A and B. With the help of half adder, one can design a circuit that is capable of performing simple addition with the help of logic gates.

Let us first take a look at the addition of single bits.

$0 + 0 = 0$

$0 + 1 = 1$

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